

Atty. Dkt. No. 059729-0117

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Teruo KAGANOI et al.
Title: PACKET PROCESSING UNIT
Appl. No.: Unassigned
Filing Date: 03/07/2002
Examiner: Unassigned
Art Unit: Unassigned

PRELIMINARY AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to examination of the present application, Applicant respectfully requests that the above-identified prior application be amended as follows:

IN THE CLAIMS

In accordance with 37 C.F.R. § 1.121, please substitute for original claims 5 and 6 the following rewritten version of the same claims, as amended. The changes are shown explicitly in the attached "Version with Markings to Show Changes Made."

5. (Once Amended) A packet processing unit as claimed in claim 1, comprising buffer means for a timing adjustment between said transmission channel and said packet receiving means and between said transmission channel and said packet transmitting means.

6. (Once Amended) A packet processing unit as claimed in claim 1, comprising arithmetic processing means for performing a predetermined process with respect to said cells in at least one of a following step to said packet receiving means or a preceding step to said packet transmitting means.

Please add the following new claims.

7. (New) A packet processing unit as claimed in claim 2, comprising buffer means for a timing adjustment between said transmission channel and said packet receiving means and between said transmission channel and said packet transmitting means.

8. (New) A packet processing unit as claimed in claim 3, comprising buffer means for a timing adjustment between said transmission channel and said packet receiving means and between said transmission channel and said packet transmitting means.

9. (New) A packet processing unit as claimed in claim 4, comprising buffer means for a timing adjustment between said transmission channel and said packet receiving means and between said transmission channel and said packet transmitting means.

10. (New) A packet processing unit as claimed in claim 2, comprising arithmetic processing means for performing a predetermined process with respect to said cells in at least one of a following step to said packet receiving means or a preceding step to said packet transmitting means.

11. (New) A packet processing unit as claimed in claim 3, comprising arithmetic processing means for performing a predetermined process with respect to said cells in at least one of a following step to said packet receiving means or a preceding step to said packet transmitting means.

12. (New) A packet processing unit as claimed in claim 4, comprising arithmetic processing means for performing a predetermined process with respect to said cells in at least one of a following step to said packet receiving means or a preceding step to said packet transmitting means.

13. (New) A packet processing unit as claimed in claim 5, comprising arithmetic processing means for performing a predetermined process with respect to said cells in at least one of a following step to said packet receiving means or a preceding step to said packet transmitting means.

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REMARKS

Applicant respectfully requests that the foregoing amendments to the claims be made prior to examination of the present application.

Respectfully submitted,

Date: March 7, 2002

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Customer Number: 22428



22428

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Version with Markings to Showing Changes Made

5. (Once Amended) A packet processing unit as claimed in [at least one of] claim 1[through 4], comprising buffer means for a timing adjustment between said transmission channel and said packet receiving means and between said transmission channel and said packet transmitting means.

6. (Once Amended) A packet processing unit as claimed in [at least one of] claim 1[through 5], comprising arithmetic processing means for performing a predetermined process with respect to said cells in at least one of a following step to said packet receiving means or a preceding step to said packet transmitting means.

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